



PATENT ABSTRACTS OF JAPAN

(11) Publication number: **2001195025 A**(43) Date of publication of application: **19.07.01**

(51) Int. Cl.

G09G 3/20
G09F 9/00
G09G 5/00

(21) Application number: **2000009163**(22) Date of filing: **18.01.00**(30) Priority: **02.11.99 JP 11312199**(71) Applicant: **SONY CORP**(72) Inventor: **MIURA SHINICHIRO**(54) **FLAT PANEL DISPLAY DEVICE**

COPYRIGHT: (C)2001,JPO

(57) Abstract:

PROBLEM TO BE SOLVED: To shift the display device into a power conservation standby state by judging a time when a user is absent properly.

SOLUTION: When a user is apart from the display device, the detection voltage of a sensor 30 is lowered and the output of a comparator 110 becomes an L level. A holding circuit 130 maintains the output of the comparator 110 to output it as a hold voltage and when the state of the L level of the comparator 110 is continued, the hold voltage is gradually lowered. Then, when the state of the L level of the comparator 110 is continued for a time equal to or longer than a fixed time, since the hold voltage becomes smaller than a decision voltage, the output of a decision device 140 is inverted to an L level. Thus, a controller part 160 judges that the need for shifting the device into a power-OFF mode is generated and the part 160 changes over the display device to the power-OFF mode by using a display control part 170.

